

Notice of References Cited	Application/Control No. 10/715,459		Applicant(s)/Patent Under Reexamination DAVIS ET AL.	
	Examiner Keith Vicary		Art Unit 2183	Page 1 of 1

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*	B	US-6,341,318	01-2002	Dakhil, Dani Y.	710/23
*	C	US-6,317,820	11-2001	Shiell et al.	712/32
*	D	US-5,577,250	11-1996	Anderson et al.	718/100
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NON-PATENT DOCUMENTS

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	U	"SPARTA: Simulation of Physics on a Real-Time Architecture"; Great Lakes Symposium on VLSI archive □□ Proceedings of the 10th Great Lakes symposium on VLSI; Pages: 177 - 182 □□ Year of Publication: 2000 ; Benjamin Bishop et al.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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